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Ch		4,348,759	09/07/82	Schnurmann		371	20		
Oh		4,857,774	08/15/89	El-Aya	El-Ayat et al.		465	<u></u>	
cb		5,068,603	11/26/91	Mahoney		324	158 R		·
e b		5,243,274	09/07/93	Kelsey et al.		324	158 R		
Ck		5,253,255	10/12/93	Carbine		371	22.6		
Cb		5,369,643	11/29/94	Rastgar et al.		371	15.1		
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Ob		5,563,830	10/08/96	Ishida		365	189.02		
90		5,712,858	01/27/98	Godiwala et al.		371	22.1		
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L						_	Date, Pertinent Pa		•
cb		Arimilli et al., "METH IBM Technical Disclosu	OD TO VERIFY ire Bulletin, Vo. 3	CHIP-TO 36, No. 06 <i>2</i>	O-CHIP INTERCONNEC A, June 1993, pp. 437–438	T WITHIN A	. HARDWARE S	SYSTEM"	
Op	A. D. Savkar, "N-WAY TESTPOINT FOR COMPLEX LSI DESIGN", IBM Techinical Bulleting, Vol. 14, No. 10, March 1972, pp.2937-2938								1arch
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OB		5,734,660	03/31/98	Fujisaki		371	22.3			
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Form PTO-A820 (also form PTO-1449)